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The textbook for the class is J.M. Rabaey, A. Chandrakasan, B. Nikolic, "Digital Integrated Circuits: A Design Perspective," 2nd edition, Prentice-Hall 2003. Maintained by Jan Rabaey and the Class TAs

EE141: Digital Integrated Circuits

Digital Integrated Circuits Rabaey Solution Prof. Rabaey has made high-impact contributions to a number of fields, including advanced wireless systems, low power integrated circuits, sensor networks, and ubiquitous computing. His current interests include the conception of the next-

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Prof. Rabaey has made high-impact contributions to a number of fields, including advanced wireless systems, low power integrated circuits, sensor networks, and ubiquitous computing. His current interests include the conception of the next-generation integrated wireless systems over a broad range of applications, as well as exploring the interaction between the cyber and the biological world.

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It focuses solely on deep-submicron CMOS devices, the workhorses of today's digital integrated circuits. A simple transistor model for manual analysis, called the unified MOS model, has been developed and is used throughout. Design Examples stress the design of Digital ICs from a real-world perspective.

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Jan Rabaey (auth.) Low Power Design Essentials is the first book at the graduate level to address the design of low power digital integrated circuits in an orderly and logical fashion. As such, this book will be of interest to students as well as professionals.

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Beginning with discussions on the operation of electronic devices and analysis of the nucleus of digital design, the text addresses: the impact of interconnect, design for low power, issues in timing and clocking, design methodologies, and the effect of design automation on the digital design perspective.

The fourth edition of CMOS Digital Integrated Circuits: Analysis and Design continues the well-established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design, as well as addressing state-of-the-art technology issues highlighted by the widespread use of nanometer-scale CMOS technologies.

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In this latest edition, virtually all chapters have been re-written, the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations, and the material has been reinforced with up-to-date examples. The broad-ranging coverage of this textbook starts with the fundamentals of CMOS process technology, and continues with MOS transistor models, basic CMOS gates, interconnect effects, dynamic circuits, memory circuits, arithmetic building blocks, clock and I/O circuits, low power design techniques, design for manufacturability and design for testability.

Linear Systems and Signals, Third Edition, has been refined

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and streamlined to deliver unparalleled coverage and clarity. It emphasizes a physical appreciation of concepts through heuristic reasoning and the use of metaphors, analogies, and creative explanations. The text uses mathematics not only to prove axiomatic theory but also to enhance physical and intuitive understanding. Hundreds of fully worked examples provide a hands-on, practical grounding of concepts and theory. Its thorough content, practical approach, and structural adaptability make *Linear Systems and Signals, Third Edition*, the ideal text for undergraduates.

Exponential improvement in functionality and performance of digital integrated circuits has revolutionized the way we live and work. The continued scaling down of MOS

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transistors has broadened the scope of use for circuit technology to the point that texts on the topic are generally lacking after a few years. The second edition of Digital Integrated Circuits: Analysis and Design focuses on timeless principles with a modern interdisciplinary view that will serve integrated circuits engineers from all disciplines for years to come. Providing a revised instructional reference for engineers involved with Very Large Scale Integrated Circuit design and fabrication, this book delves into the dramatic advances in the field, including new applications and changes in the physics of operation made possible by relentless miniaturization. This book was conceived in the versatile spirit of the field to bridge a void that had existed between books on transistor electronics and those covering

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VLSI design and fabrication as a separate topic. Like the first edition, this volume is a crucial link for integrated circuit engineers and those studying the field, supplying the cross-disciplinary connections they require for guidance in more advanced work. For pedagogical reasons, the author uses SPICE level 1 computer simulation models but introduces BSIM models that are indispensable for VLSI design. This enables users to develop a strong and intuitive sense of device and circuit design by drawing direct connections between the hand analysis and the SPICE models. With four new chapters, more than 200 new illustrations, numerous worked examples, case studies, and support provided on a dynamic website, this text significantly expands concepts presented in the first edition.

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Contains the most extensive coverage of digital integrated circuits available in a single source. Provides complete qualitative descriptions of circuit operation followed by in-depth analytical analyses and spice simulations. The circuit families described in detail are transistor-transistor logic (TTL, STTL, and ASTTL), emitter-coupled logic (ECL), NMOS logic, CMOS logic, dynamic CMOS, BiCMOS structures and various GASFET technologies. In addition to detailed presentation of the basic inverter circuits for each digital logic family, complete details of other logic circuits for these families are presented.

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With vastly increased complexity and functionality in the "nanometer era" (i.e. hundreds of millions of transistors on one chip), increasing the performance of integrated circuits has become a challenging task. Connecting effectively (interconnect design) all of these chip elements has become the greatest determining factor in overall performance. 3-D integrated circuit design may offer the best solutions in the near future. This is the first book on 3-D integrated circuit design, covering all of the technological and design aspects of this emerging design paradigm, while proposing effective solutions to specific challenging problems concerning the design of 3-D integrated circuits. A handy, comprehensive reference or a practical design guide, this book provides a

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sound foundation for the design of 3-D integrated circuits. * Demonstrates how to overcome "interconnect bottleneck" with 3-D integrated circuit design...leading edge design techniques offer solutions to problems (performance/power consumption/price) faced by all circuit designers * The FIRST book on 3-D integrated circuit design...provides up-to-date information that is otherwise difficult to find * Focuses on design issues key to the product development cycle...good design plays a major role in exploiting the implementation flexibilities offered in the 3-D * Provides broad coverage of 3-D integrated circuit design, including interconnect prediction models, thermal management techniques, and timing optimization...offers practical view of designing 3-D circuits

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This book contains all the topics of importance to the low power designer. It first lays the foundation and then goes on to detail the design process. The book also discusses such special topics as power management and modal design, ultra low power, and low power design methodology and flows. In addition, coverage includes projections of the future and case studies.

Low Power Design Methodologies presents the first in-depth coverage of all the layers of the design hierarchy, ranging from the technology, circuit, logic and architectural levels, up to the system layer. The book gives insight into the mechanisms of power dissipation in digital circuits and

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presents state of the art approaches to power reduction. Finally, it introduces a global view of low power design methodologies and how these are being captured in the latest design automation environments. The individual chapters are written by the leading researchers in the area, drawn from both industry and academia. Extensive references are included at the end of each chapter. Audience: A broad introduction for anyone interested in low power design. Can also be used as a text book for an advanced graduate class. A starting point for any aspiring researcher.

Electrical Engineering 101 covers the basic theory and practice of electronics, starting by answering the question "What is electricity?" It goes on to explain the fundamental

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principles and components, relating them constantly to real-world examples. Sections on tools and troubleshooting give engineers deeper understanding and the know-how to create and maintain their own electronic design projects. Unlike other books that simply describe electronics and provide step-by-step build instructions, EE101 delves into how and why electricity and electronics work, giving the reader the tools to take their electronics education to the next level. It is written in a down-to-earth style and explains jargon, technical terms and schematics as they arise. The author builds a genuine understanding of the fundamentals and shows how they can be applied to a range of engineering problems. This third edition includes more real-world examples and a glossary of formulae. It contains new

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coverage of: Microcontrollers FPGAs Classes of components
Memory (RAM, ROM, etc.) Surface mount High speed design
Board layout Advanced digital electronics (e.g. processors)
Transistor circuits and circuit design Op-amp and logic
circuits Use of test equipment Gives readers a simple
explanation of complex concepts, in terms they can
understand and relate to everyday life. Updated content
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