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Addressing modes of 8086

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Language program on 8086

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XLAT (Translate)

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Microprocessor Instruction
Set Of 8086 Microprocessor
The 8086 microprocessor
supports 8 types of
instructions ? Data Transfer
Instructions; Arithmetic
Instructions; Bit

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Manipulation Instructions;
String Instructions; Program
Execution Transfer
Instructions (Branch & Loop
Instructions) Processor
Control Instructions;
Iteration Control
Instructions; Interrupt

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Instructions

Microprocessor - 8086

Instruction Sets -

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Examples 1. Data Transfer

Instructions: . Data Transfer

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Instructions defines the data transfer operation. MOV reg1, reg2 The... 2.

Arithmetic Instructions:.

ADD reg1, reg2 In the instruction ADD AL, BL, the content of BL is added with the content... 3. Logical

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...

Instruction Set of 8086 with
Examples - Electronics and

...

Instruction set of 8086
microprocessor Data Transfer
Instruction. This group

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includes the instructions used for moving the data from one place to another.

In... Arithmetic

Instructions. These

instructions are used in order to execute arithmetic instructions like

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addition,... Logical
Instructions. ...

Instruction Set of 8086
Microprocessor - Electronics
Desk

What are instruction sets?
The 8086 microprocessor

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supports 8 types of
instructions ? Data Transfer
Instructions; Arithmetic
Instructions; Bit
Manipulation Instructions;
String Instructions; Program
Execution Transfer
Instructions (Branch & Loop

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Instructions) Processor
Control Instructions;
Iteration Control
Instructions; Interrupt
Instructions

What are instruction sets in
microprocessor 8086?

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Instruction Format of 8086

The instructions in 8086 are 1 to 7 byte long depending on the addressing mode. The immediate byte may be 8 or 16 bit, and the displacement in an instruction can be 0 bits or 8 bits or 16 bits.

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The OP CODE and addressing mode designation may be 1 to 2 byte.

8086 Instruction Format-I

8086 Microprocessor -

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Instruction Set of 8086 Data

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Transfer instruction. All the instructions which perform data movement come under this category. The source data... Arithmetic Instructions. Instructions of this group perform addition, subtraction,

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multiplication, division,
increment, ... Logical
Instructions. ...

Instruction Set of 8086 -
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1. Data Transfer

Instructions Instruction Set

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2 8086 Microprocessor

Instructions that are used to transfer data/ address in to registers, memory locations and I/O ports.

Generally involve two operands: Source operand and Destination operand of the

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same size. Source: Register
or a memory location or an
immediate data Destination:
Register or a memory
location.

instruction set of 8086.pdf
- 8086 Microprocessor ...

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8086 is a 16-bit microprocessor and address bus of 20-bit size. In this tutorial, we will see internal architecture of 8086 microprocessor.

Firstly, we will discuss bus interface unit part and

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secondly, we will discuss execution unit part along with details of general purpose and special function registers.

8086 Microprocessor
Architecture -

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8086 microprocessor Intel

8086. Intel 8086

microprocessor is the

enhanced version of Intel

8085 microprocessor. It was

designed by Intel in... 8086

pins configuration. AD0-AD15

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(Address Data Bus):

Bidirectional address/data lines. These are low order address bus. Operating Modes of 8086. There are ...

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x86 integer instructions.
This is the full 8086/8088
instruction set of Intel.
Most if not all of these
instructions are available
in 32-bit mode; they just
operate on 32-bit registers
(eax, ebx, etc.) and values

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instead of their 16-bit (ax, bx, etc.) counterparts. See also x86 assembly language for a quick tutorial for this processor family. The updated instruction set is also grouped ...

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x86 instruction listings -
Wikipedia

INSTRUCTION SET OF 8086. The
8086 instructions are
categorized into the
following main types. 1.
Data Copy / Transfer
Instructions. 2. Arithmetic

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and Logical Instructions. 3.
Shift and Rotate
Instructions. 4. Loop
Instructions. 5. Branch
Instructions. 6. String
Instructions. 7. Flag
Manipulation Instructions.
8.

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Instruction set and
assembler directives of 8086

...

The contents of the AH
register are copied into the
lower byte of the Data
Transfer Instructions in

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8086 flag register. PUSHF
Instruction : Push flag
register on the stack. This
instruction decrements the
stack pointer by 2 and
copies the word in the flag
register to the memory
location(s) pointed by the

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stack pointer.

Data Transfer Instructions
in 8086 | Word Transfer ...

Let us take a look at the
programming of 8085

Microprocessor. Instruction
sets are instruction codes

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to perform some task. It is classified into five categories. Write a program to arrange first 10 numbers from memory address 3000H in an ascending order. MVI B, 09 : "Initialize counter"
START : "LXI H ...

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Microprocessor - 8085

Instruction Sets -

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The syntax of MOV

instruction is: MOV

Destination, Source. It

copies the data of 2 nd

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operand (source) into the 1st operand (destination). To access memory, segment registers are used along with general-purpose registers. If you don't know about internal register of 8086 microprocessor, read

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this article: 8086
Microprocessor Architecture

8086 Microprocessor
Addressing Modes Explained
with ...

8086 microprocessor
instruction set

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The 8086 (also called iAPX
86) is a 16-bit
microprocessor chip designed
by Intel between early 1976
and June 8, 1978, when it

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was released. The Intel 8088, released July 1, 1979, is a slightly modified chip with an external 8-bit data bus (allowing the use of cheaper and fewer supporting ICs), and is notable as the processor used in the

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original IBM PC design.

Intel 8086 - Wikipedia
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8086 INSTRUCTION SET DATA
TRANSFER INSTRUCTIONS MOV -
MOV Destination, Source The
MOV instruction copies a
word or byte of data from a
specified source to a
specified destination. The

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destination can be a register or a memory location. The source can be a register, a memory location or an immediate number.

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